

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

New York, New York

Milton J. BODEN, Jr.

Date: March 31, 2004

Serial No.: ---

Group Art Unit: ---

Filed:--

Examiner: ---

For: RAD MOSFET WITH GRADED BODY DIODE JUNCTION AND REDUCED ON
RESISTANCE

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Submitted herewith is a copy of art together with a form listing the same for the convenience of the Examiner.

EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail (mail label #**EV3436784441** in an envelope addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on **March 31, 2004**.

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Name of applicant, assignee or
Registered Representative

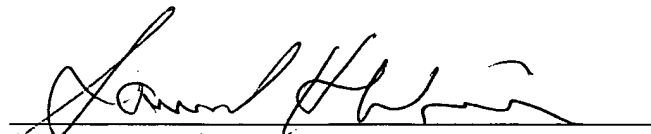


Signature

March 31, 2004
Date of Signature

SHW/CP:db
Enclosures

Respectfully submitted,



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APPLICANT'S ART CITATION (Use several sheets if necessary)		Application		OFGS File No. IR-2719 (2-3978)	
		Applicant Milton BODEN, Jr.			
		Filing Date		Group Art Unit	

U.S. PATENT DOCUMENTS (NOT ENCLOSED)						
Examiner Initial	Document Number	Date MM-YYYY	Name	Class	Sub-class	Filing Date If Appropriate
	US-4,593,302	06-1986	Lidow et al.	257	342	
	US-5,322,804	06-1994	Beasom, James D.	438	227	
	US-5,338,693	08-1994	Kinzer et al.	438	268	
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	US-5,770,880	06-1998	Woodbury et al.	257	336	
	US-2003/0197220 A1	10-2003	Disney, Donald Ray	257	328	
	US-					
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FOREIGN PATENT DOCUMENTS							
	Document Number	Date MM-YYYY	Country	Class	Sub-class	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		

Examiner	Date Considered
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